TOSHIBA-0024250

Fig. 1
(Prior Art)

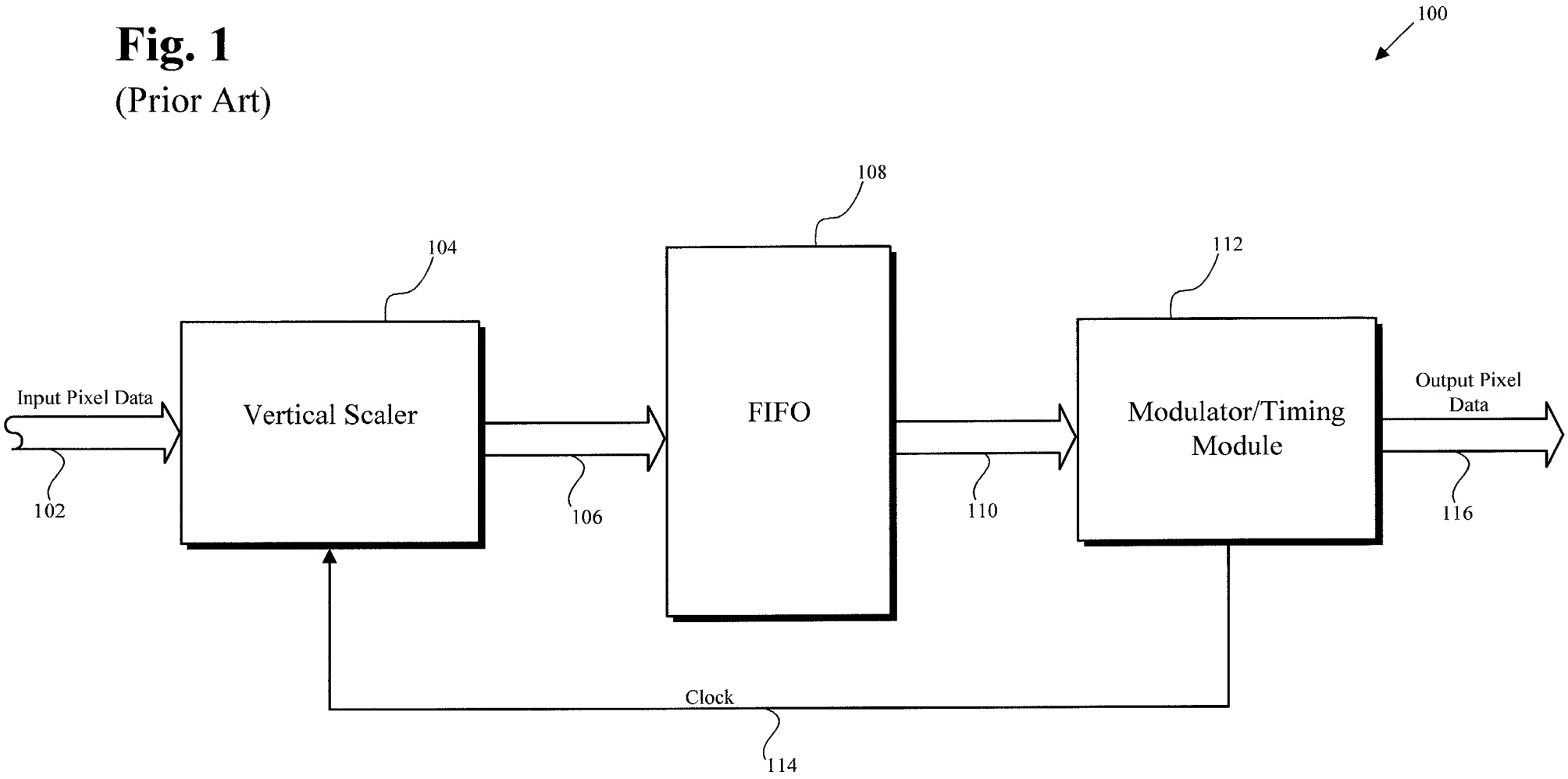


Fig. 2

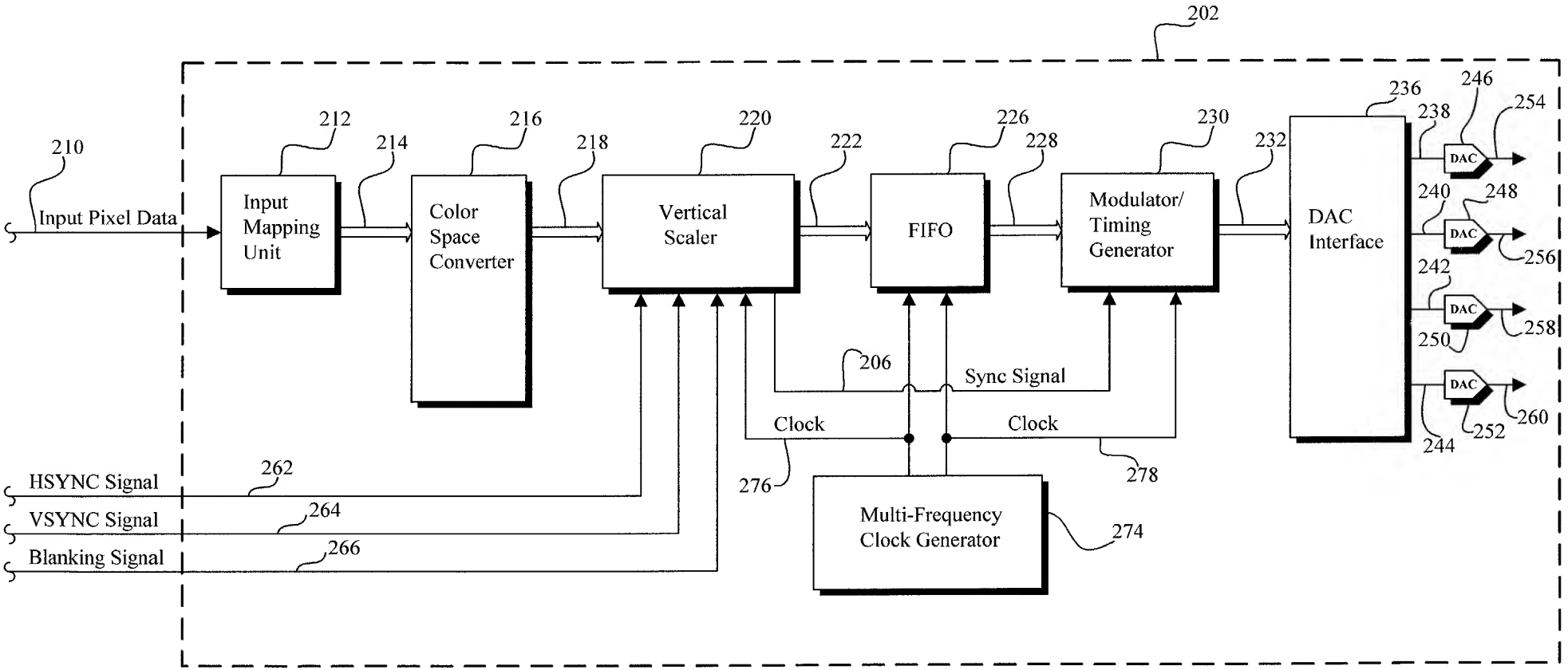

202
210
Input Pixel Data
212
Input Mapping Unit
214
216
Color Space Converter
218
220
Vertical Scaler
222
226
FIFO
228
230
Modulator/ Timing Generator
232
236
DAC Interface
238
246
254
DAC
240
248
256
242
258
250
260
252
244
206
Sync Signal
Clock
Clock
276
278
HSYNC Signal
262
VSYNC Signal
264
Blanking Signal
266
Multi-Frequency Clock Generator
274

Fig. 3A

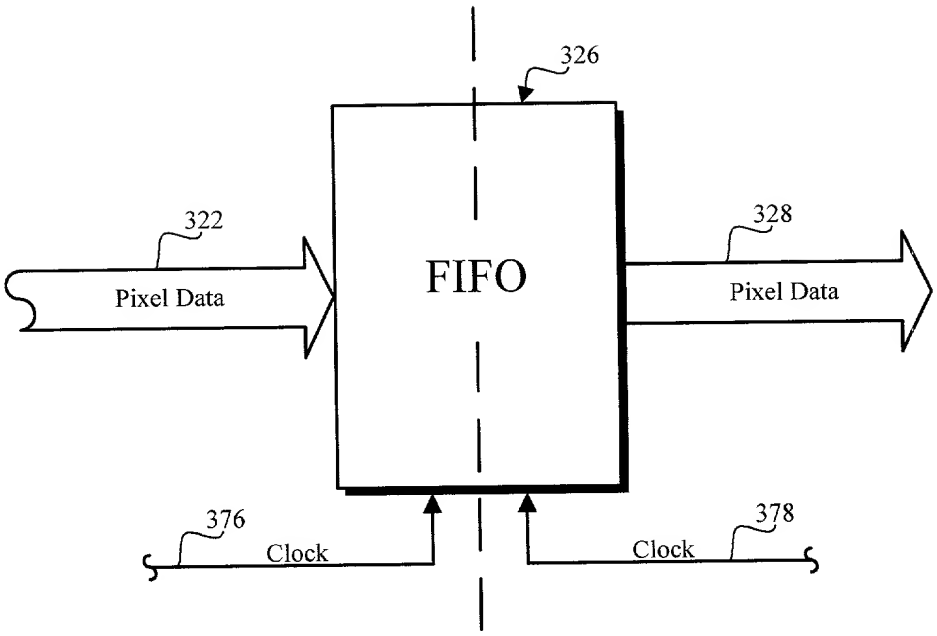
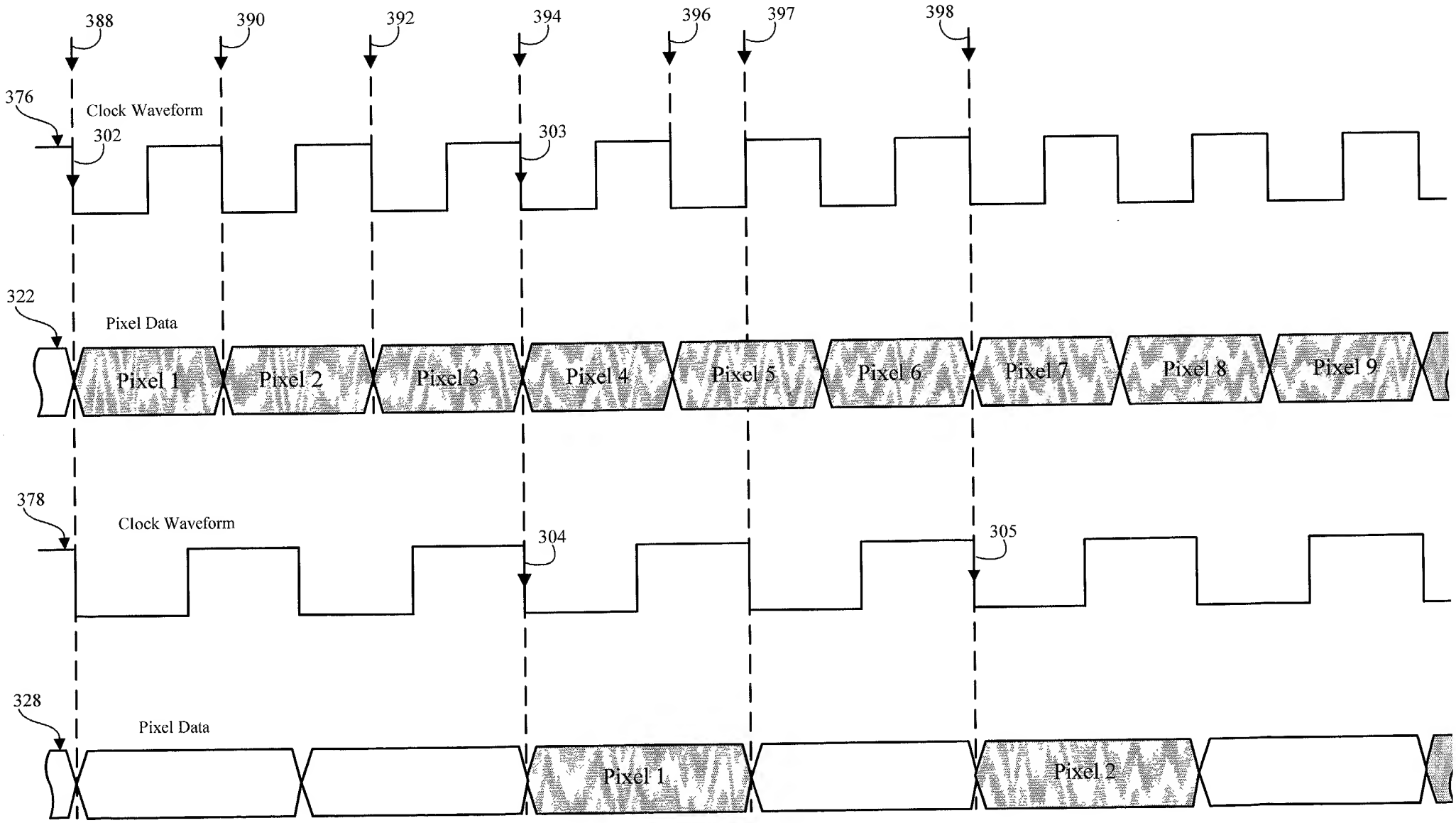


Fig. 3B



Pixel 1